

CLASS	22	Subclass	35	ISI CLASSIFICATION	556618
UTILITY SERIAL NUMBER	08 371973	PATENT DATE		07/15/1990	PATENT NUMBER
SERIAL NUMBER	08/371973	FILING DATE	01/12/95	CLASS	371
			RULE 60	SUBCLASS	221
				GROUP ART UNIT	2306

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CONTINUING DATA***

VERIFIED THIS APPLN IS A CON OF 07/869,851 04/16/92 PAT
 WHICH IS A CON OF 07/458,507 12/28/89 *abando*
SWB
 WHICH IS A DIV OF 07/140,628 01/04/88 PAT
 WHICH IS A CON OF 06/562,369 12/19/83 *abando*
 WHICH IS A CIP OF 06/368,162 04/14/82 PAT

*FOREIGN/PCT APPLICATIONS*****

VERIFIED JAPAN 220083/82 12/17/82

SHB

Foreign priority claimed 35 USC 119 conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	AS FILED	STATE OR COUNTRY	16	SHEETS DRWGS.	TOTAL CLAIMS	1	INDEP. CLAIMS	FILING FEE RECEIVED	AT DC
Verified and Acknowledged	<i>SWB</i>		Examiner's initials	JPX						\$3,288.00	50

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TITLE SEMICONDUCTOR INTEGRATED CIRCUIT

TITLE		U.S. DEPT. OF COMM./PAT. & TM CERTIFICATE JUN 24 1997	
TITLE		U.S. DEPT. OF COMM./PAT. & TM Applications Examiner	

NOTICE OF ALLOWANCE MAILED		OR CORRECTION		CLAIMS ALLOWED	
<i>H-2-96</i>		Assistant Examiner		Total Claims 118 Print Cl 1	

ISSUE FEE		<i>SM Baker</i>		DRAWING	
Amount Due	Date Paid	STEPHEN M. BAKER PRIMARY EXAMINER GROUP 2300 Primary Examiner		Sheets Drwg. 16 Figs. Drwg. 4	
<i>1850.00</i>	<i>5/2/96</i>				

Label Area		PREPARED FOR ISSUE		ISSUE BATCH NUMBER <i>N/S</i>	
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PATENT NUMBER

ORIGINAL CLASSIFICATION

CLASS

SUBCLASS

~~371~~~~221~~

APPLICATION SERIAL NUMBER

08/371973

CROSS REFERENCE(S)

CLASS

SUBCLASS
(ONE SUBCLASS PER BLOCK)

APPLICANT'S NAME (PLEASE PRINT)

IF REISSUE, ORIGINAL PATENT NUMBER

INTERNATIONAL CLASSIFICATION

CO 1 R	31/30
CO 1 R	31/3173

GROUP
ART UNIT

ASSISTANT EXAMINER (PLEASE STAMP OR PRINT FULL NAME)

2306

PRIMARY EXAMINER (PLEASE STAMP OR PRINT FULL NAME)

STEPHEN M. BAKER

PTO 270
(REV. 5-91)

ISSUE CLASSIFICATION SLIP

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE